



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7464

SERIAL NUMBER 10/634,989	FILING DATE 08/06/2003  RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 61282-034
-----------------------------	---------------------------------------	--------------	------------------------	-------------------------------------

## APPLICANTS

Mitsumi Ito, Nagaokakyo-shi, JAPAN;

Junichi Shimada, Osaka, JAPAN;

Kiyohito Mukai, Osaka, JAPAN; Hiroyuki Tsujikawa, Kusatsu-shi, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN P. 2002-229216 08/06/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 11/06/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 19	TOTAL CLAIMS 21	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

## ADDRESS

20277

MCDERMOTT WILL &amp; EMERY LLP

600 13TH STREET, N.W.

WASHINGTON, DC

20005-3096

## TITLE

Semiconductor device, method of generating pattern for semiconductor device, method of manufacturing semiconductor device and device for generating pattern used for semiconductor device

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )